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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10059422	01/31/2002	438	752	2814	T. DOAN

**APPLICANTS: Bojarczuk Nestor; Copel Matthew; Guha Supratik; Narayanan Vijay;

**CONTINUING DATA VERIFIED: YES, TD
THIS APPLICATION IS A CIP OF 09/898,039 07/05/2001

** FOREIGN APPLICATIONS VERIFIED: NONE, TD

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	YOR920010368US2
Verified and Acknowledged Examiners's initials TD		
TITLE : Method of forming lattice-matched structure on silicon and structure formed thereby		

U.S. DEPT. OF COMM./PAT. & TM.-PTO. 436L (Rev. 12-91)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
		Print Fig.	
Primary Examiner		Application Examiner	
PREPARED FOR ISSUE			
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